

# Silicon Strip Detector: From Construction to Operation of the Belle II SVD

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2026.02.06 Particle Detector Workshop 2026

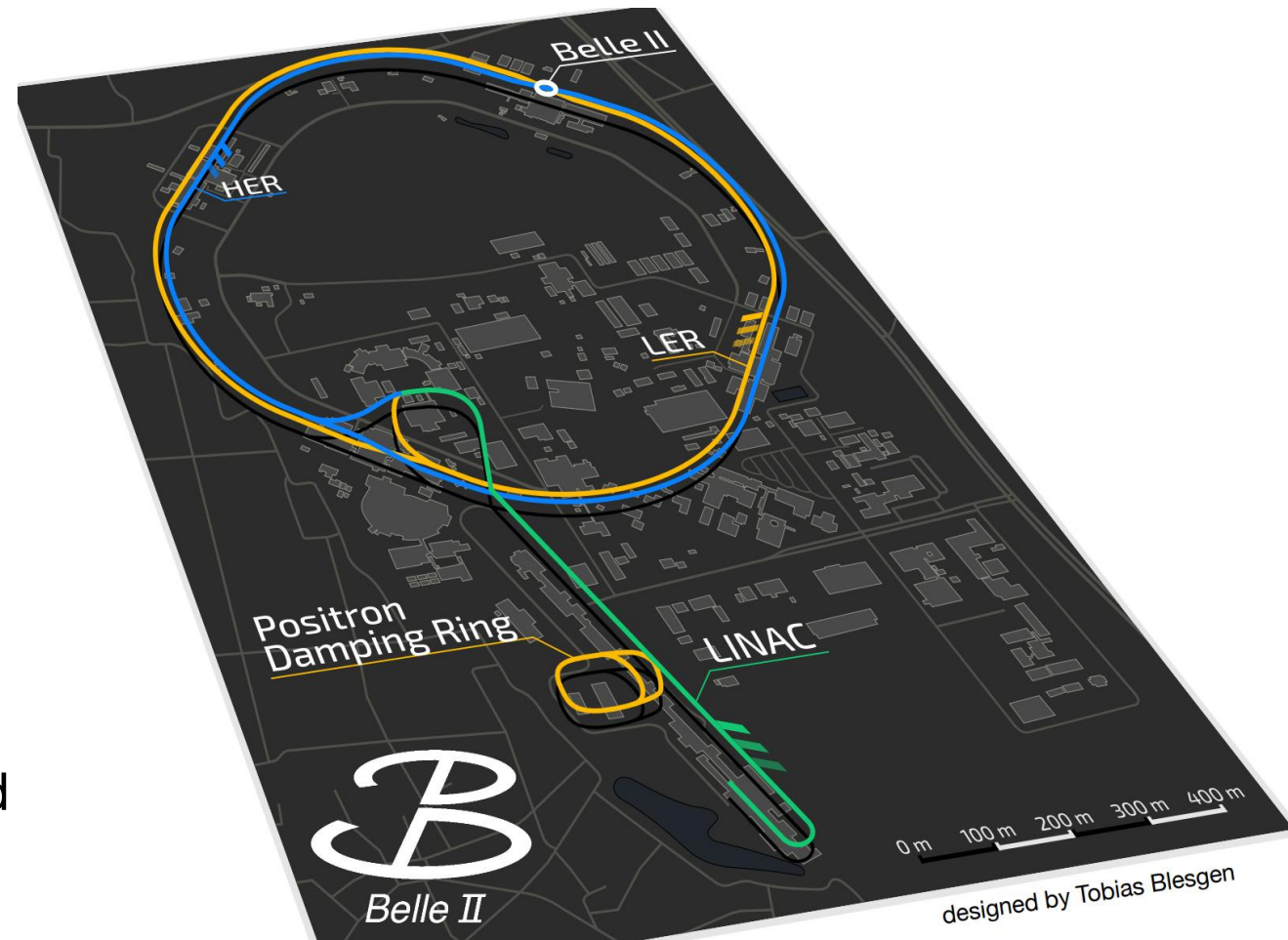


# Outline

- Silicon Vertex Detector (SVD) for Belle II experiment
- Design & Construction
- ~~Operation & Performance~~
- Summary

# SuperKEKB and Belle II

- SuperKEKB collider (KEK, Japan)
  - Asymmetric energy ( $4\text{ GeV } e^+$  &  $7\text{ GeV } e^-$ )
  - CM energy:  $10.58\text{ GeV}$  for  $\Upsilon(4S)$
  - Nano beam scheme
  - Target luminosity:  $6 \times 10^{35}\text{ cm}^{-2}\text{s}^{-1}$
  - Target Integrated  $L$ :  $50\text{ ab}^{-1}$
- B factory at luminosity frontier  $\leftrightarrow$  LHC
  - Search for new physics with B, charm, and  $\tau$  rare decays
- So far  $\sim 599/\text{fb}^{-1}$  are collected since March 2019
  - $\sim 5.1 \times 10^{34}\text{ cm}^{-2}\text{s}^{-1}$ , the new world record luminosity more than twice of KEK



# Belle II Silicon vertex detector

- Vertex detector (VXD)
  - 2 layers pixel detector (PXD)
  - 4 layers silicon vertex detector (SVD)
- Double-sided Silicon Strip Detector
- Ladders
- Material budget
- Cooling
- Construction
  - Wire bonding & Gluing
- Re-installation @ LS1

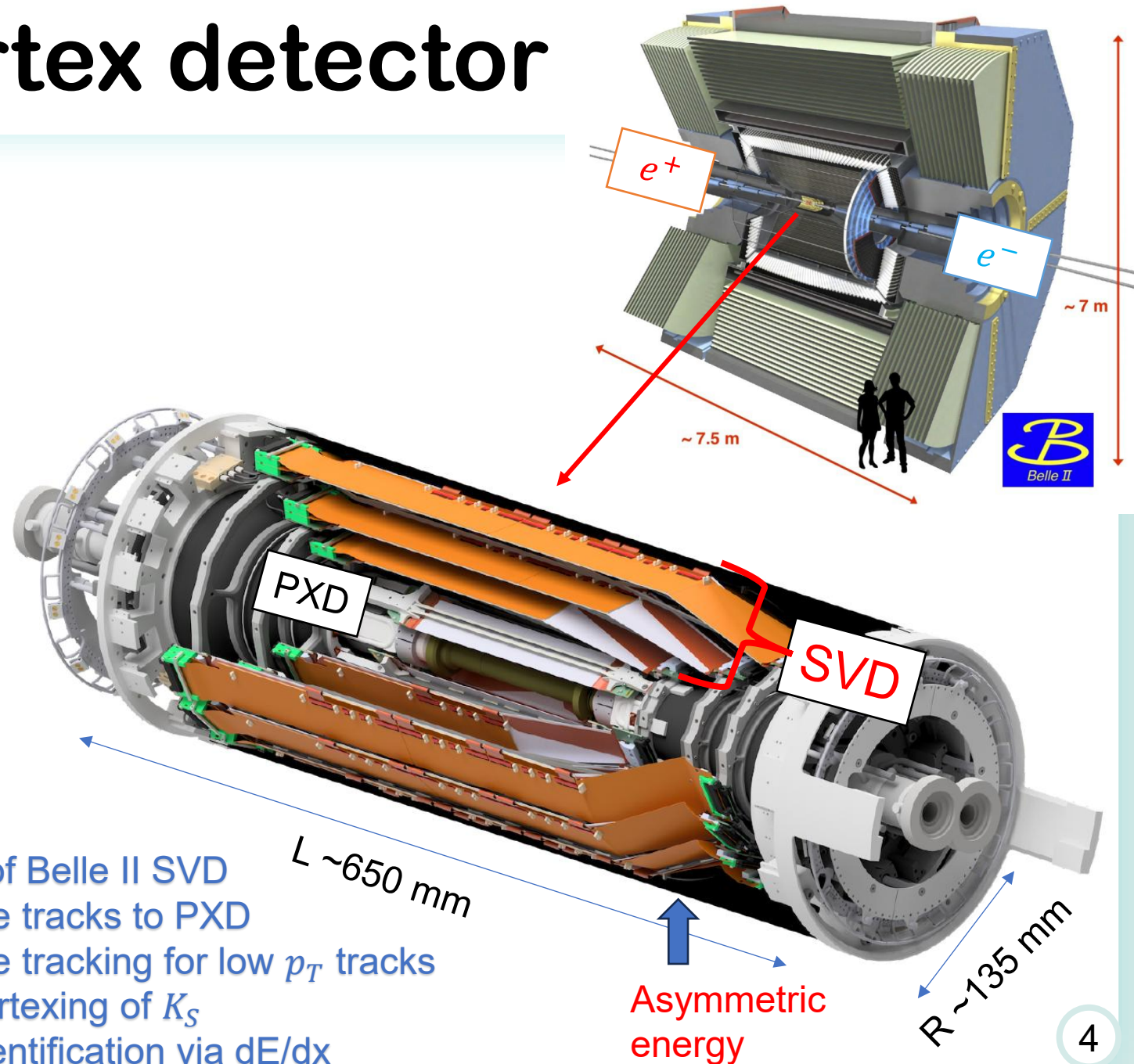
## Main features of Belle II SVD

Extrapolate tracks to PXD

Standalone tracking for low  $p_T$  tracks

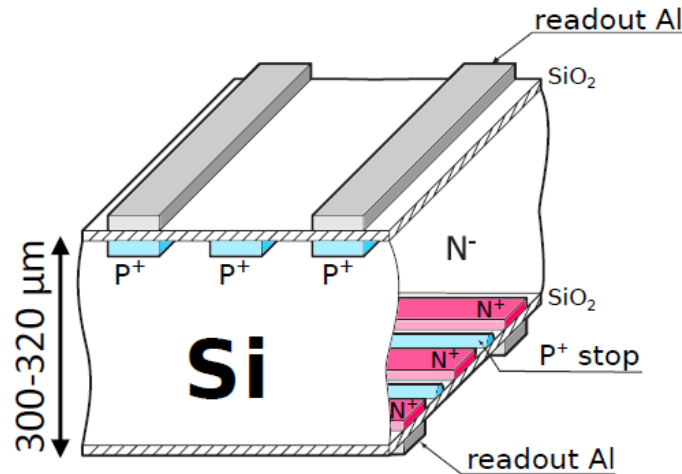
Precise vertexing of  $K_S$

Particle identification via  $dE/dx$

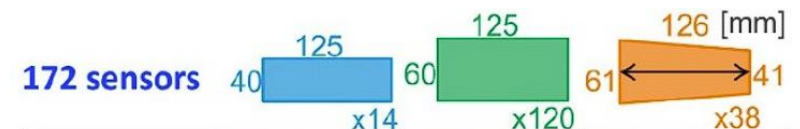
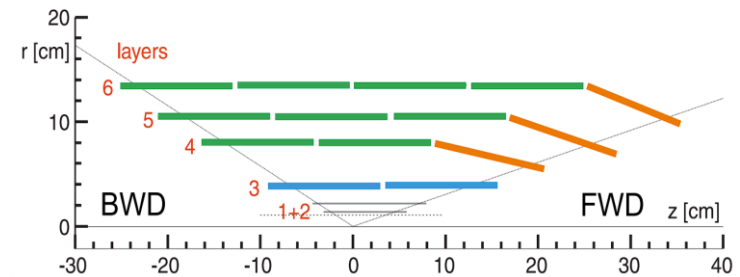


# Design and Construction

- Vertex detector (VXD)
  - 2 PXD
  - 4 layers SVD
- **Double-sided Silicon Strip Detector** →
- Ladders
- Material budget
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- Double-sided Silicon Strip Detector (DSSD)
  - AC coupled strips
  - Depletion Voltage: 20-60 V
  - Operation Voltage: 100 V



	Small	Large	Trapezoidal
# of p-strips*	768	768	768
p-strip pitch*	50 μm	75 μm	50-75 μm
# of n-strips*	768	512	512
n-strip pitch*	160 μm	240 μm	240 μm
thickness	320 μm	320 μm	300 μm
manufacturer	HPK		Micron

\*readout strips – one floating strip on both sides

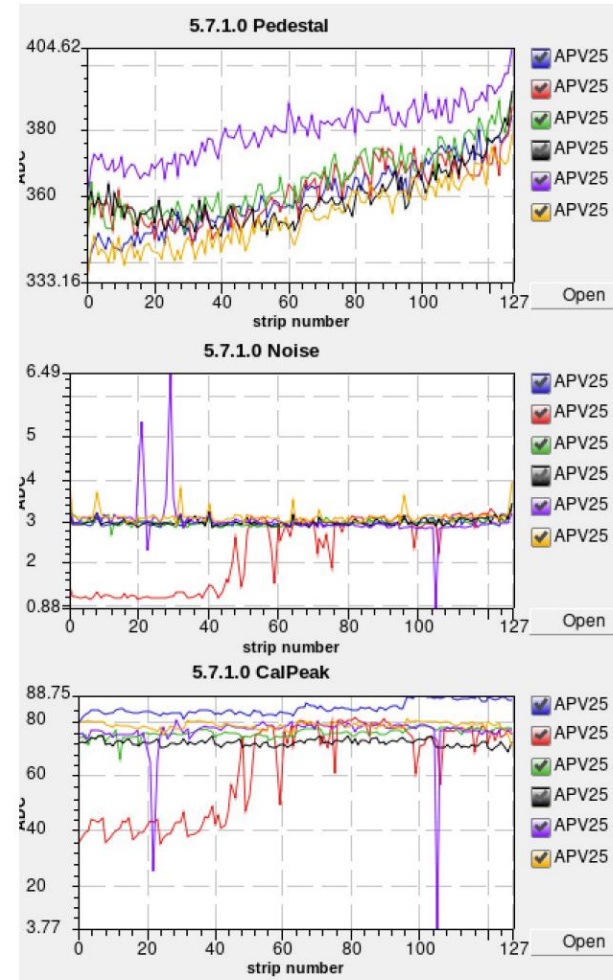


# Design and Construction

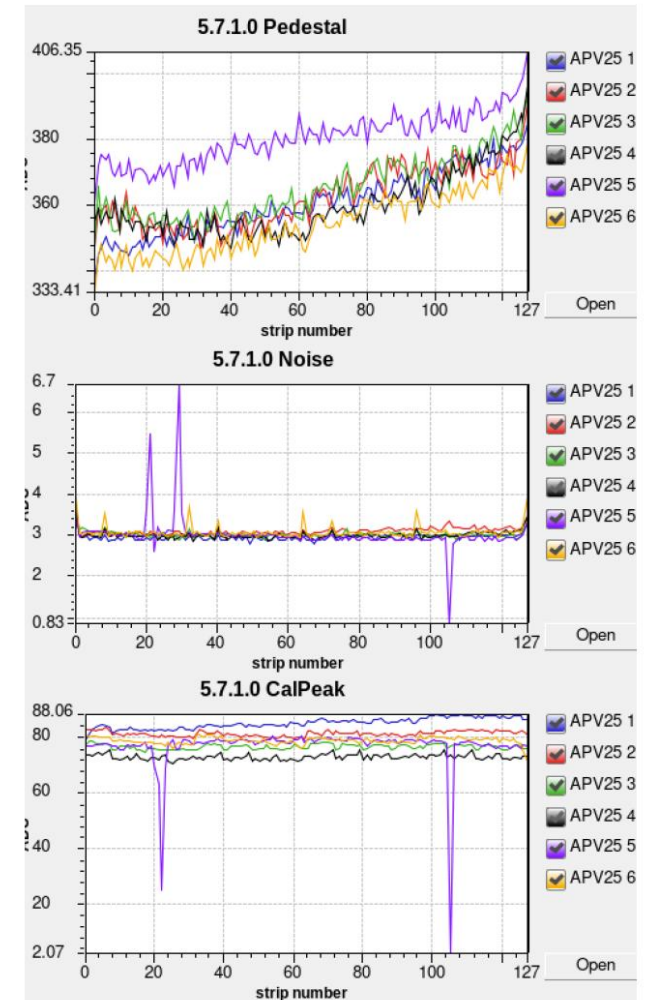
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Aug. 27th, nominal HV, Vsep



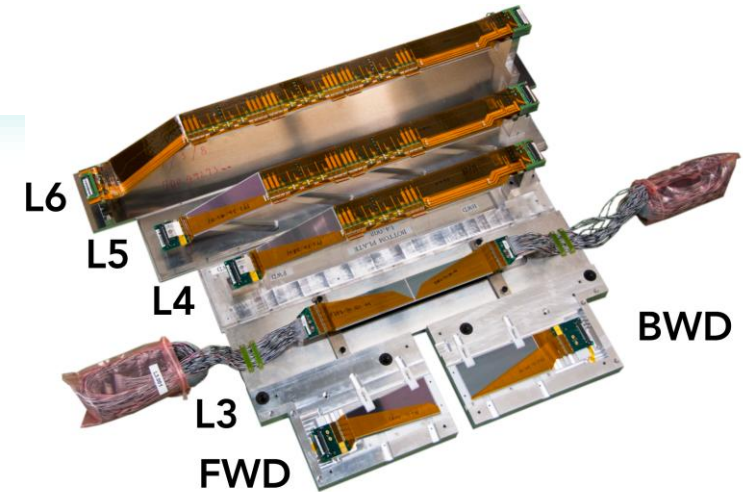
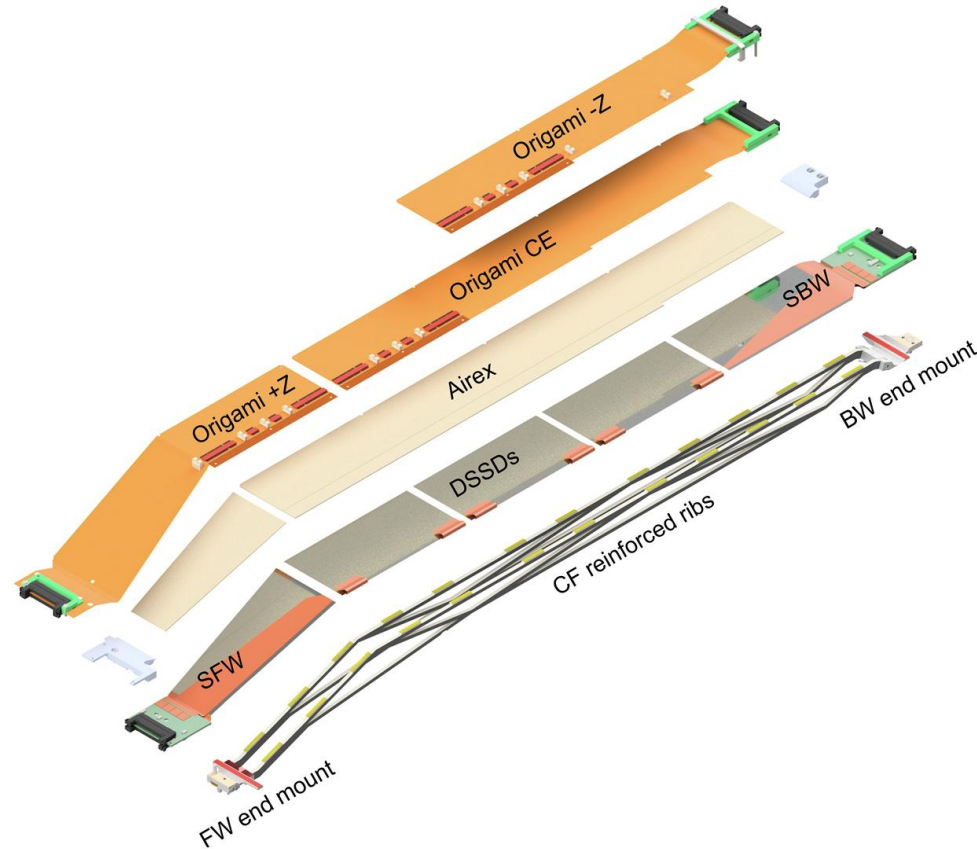
Aug. 29th, nominal HV, Vsep, N2 on.



Humidity sensitivity of Micron sensors was causing the shorts

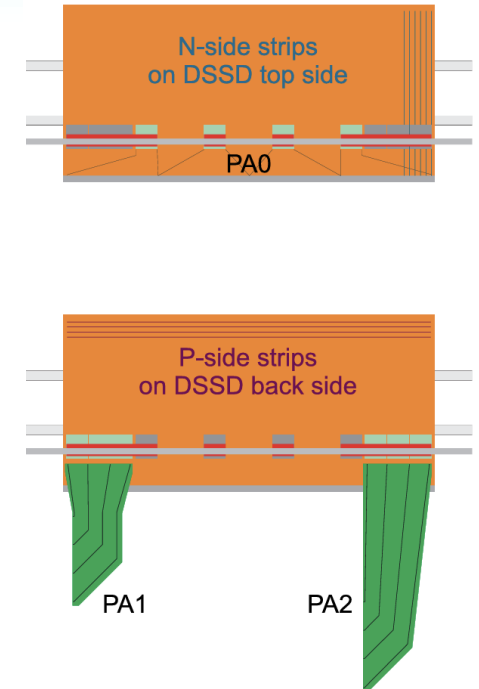
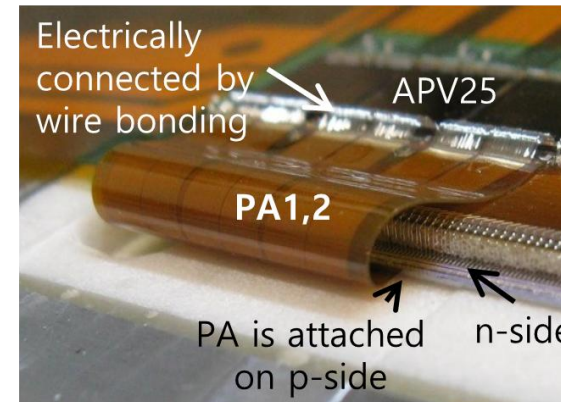
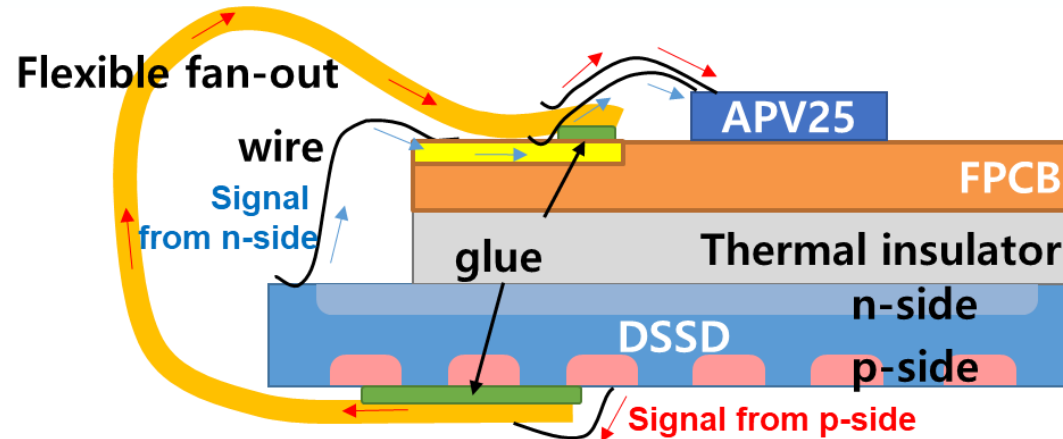
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- Material budget
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# Design and Construction

- Vertex detector (VXD)
  - 2 PXD
  - 4 layers SVD
- Double-sided Silicon Strip Detector
- Ladders
- **Material budget**  
→ “Origami”
- Cooling
- Construction
  - Wire bonding & Gluing
- Re-installation @ LS1

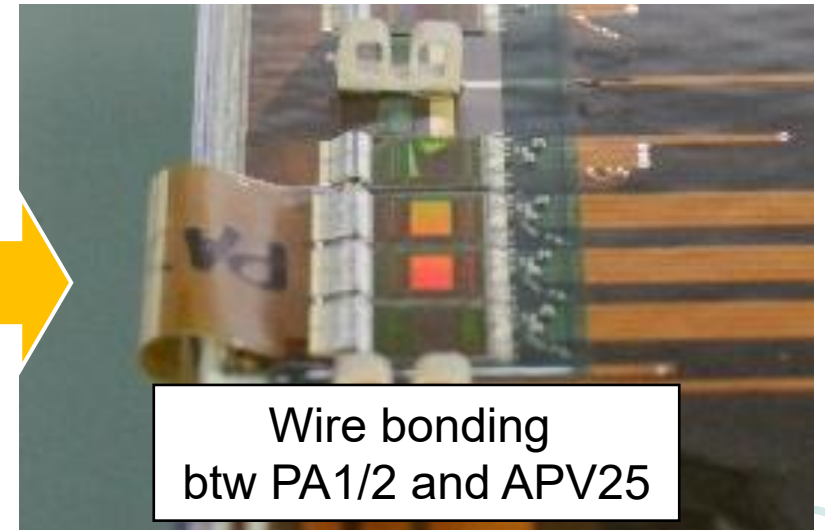
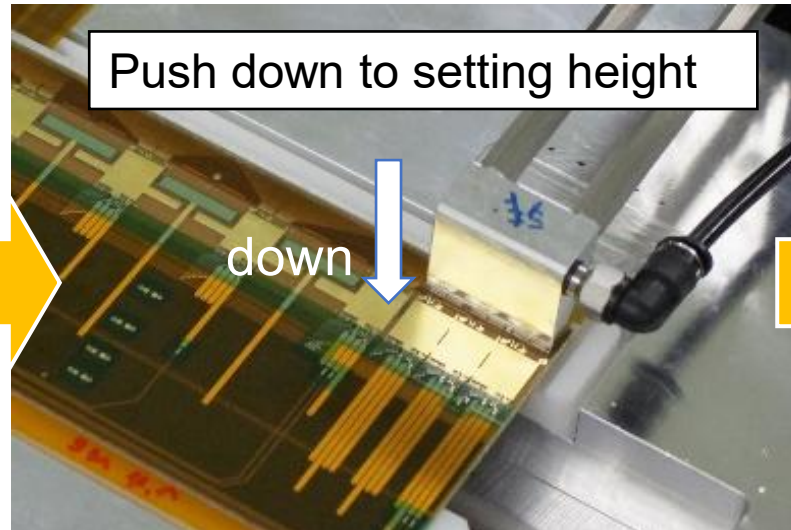
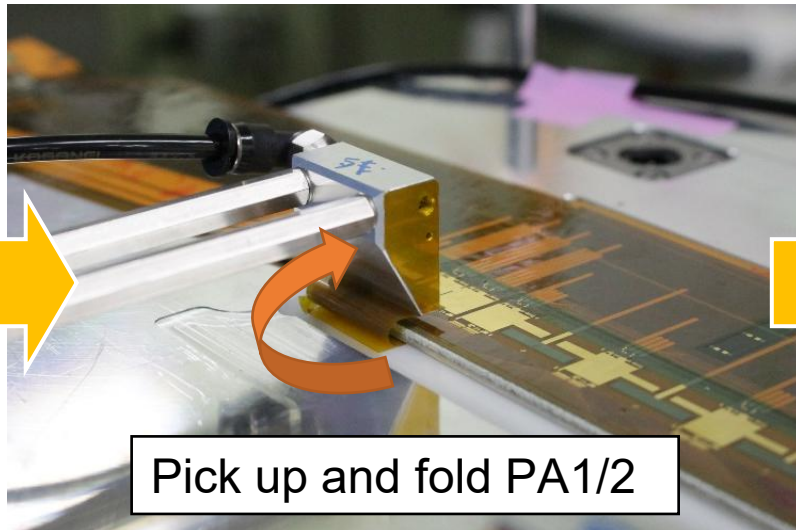
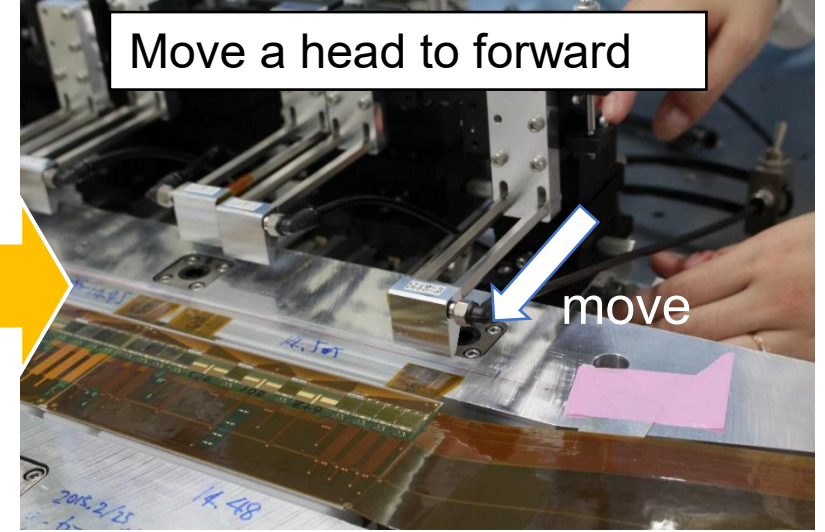
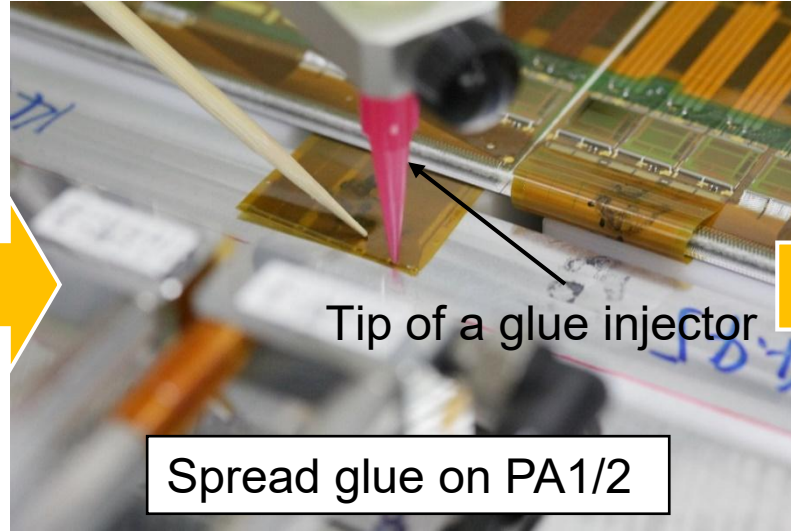
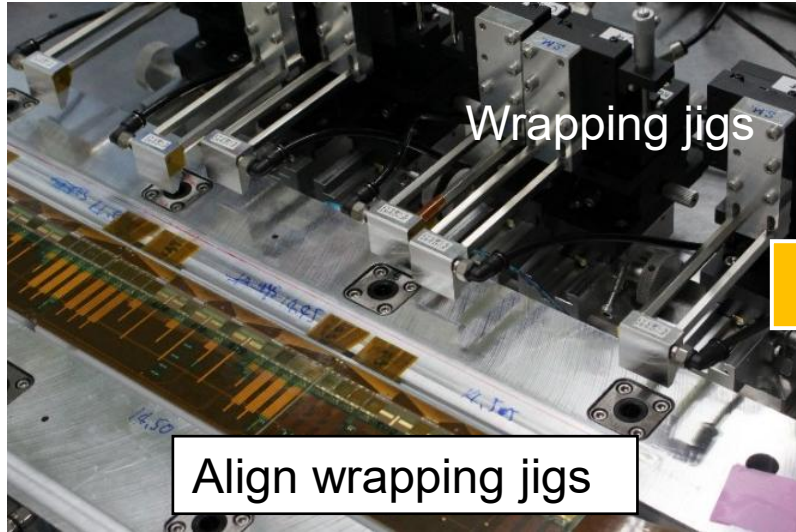


Low material budget:  $0.7\%X_0$  per layer

Low noise due to the short sensor–APV25 connection length



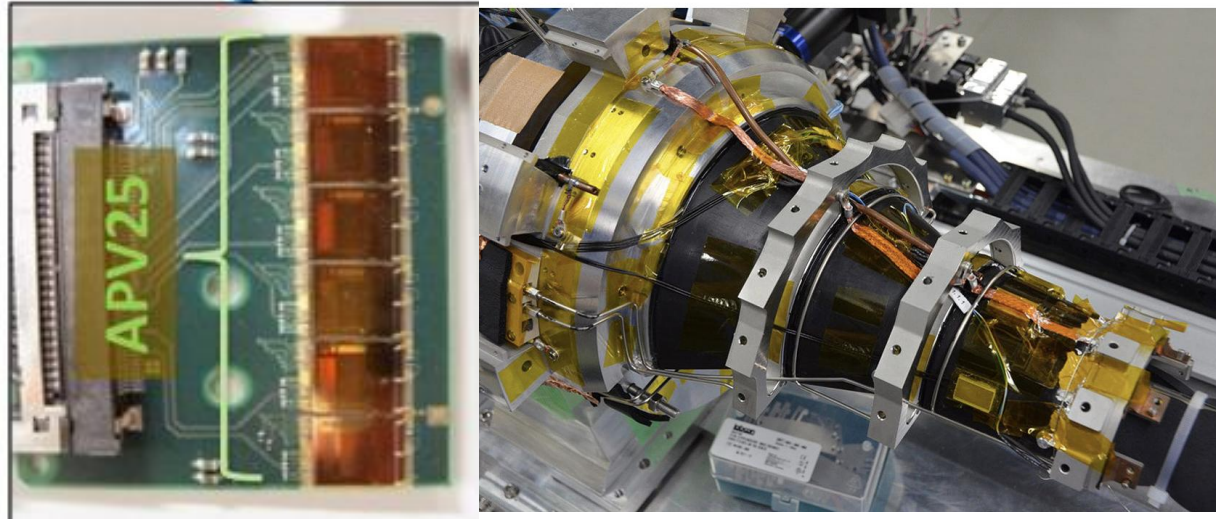
# Wrapping Procedure





# Design and Construction

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- **Cooling** →
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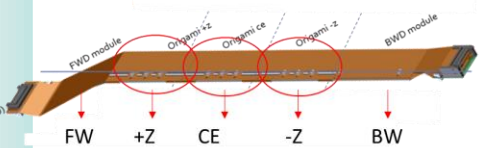
CO2 dual phase cooling -25 degC → APV25 temp. = ~10-20  
Also thanks to Origami structure → safe & reduced pile line



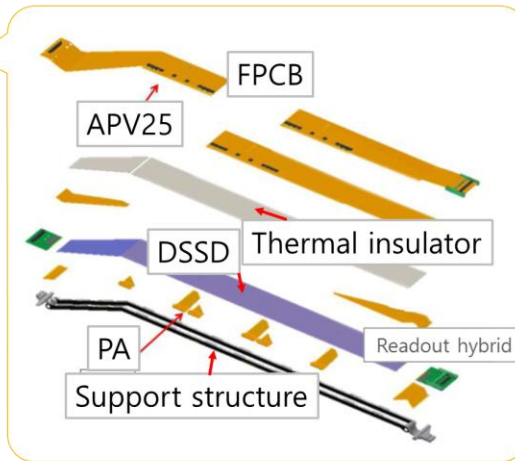
# SVD – construction



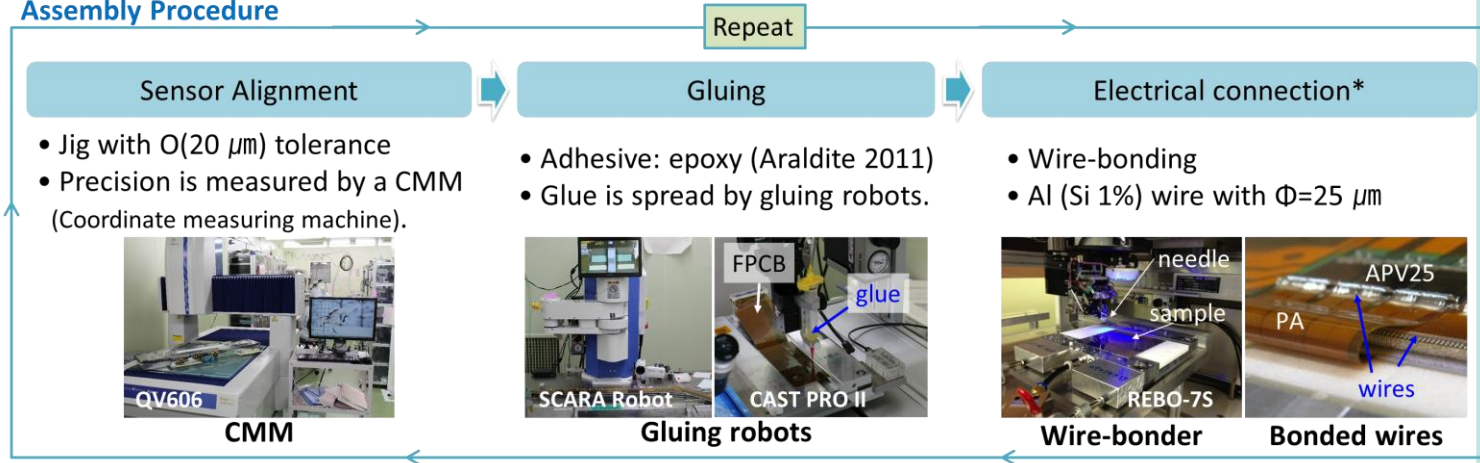
## Components of the L6 ladder



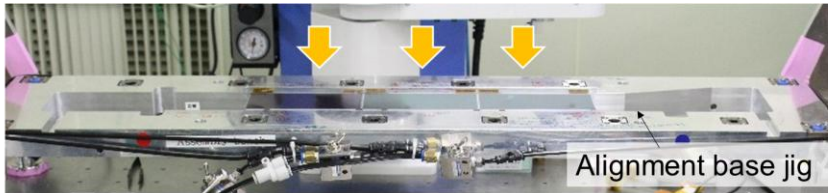
- 5 DSSD modules on L6
- Components
  - Hybrid board (2)
  - FPCB (3)
  - Flexible pitch adapter (PA)
  - DSSD (5)
  - Support structure (2)
  - etc.



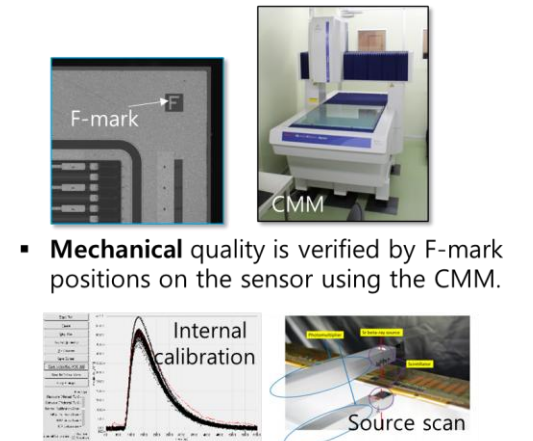
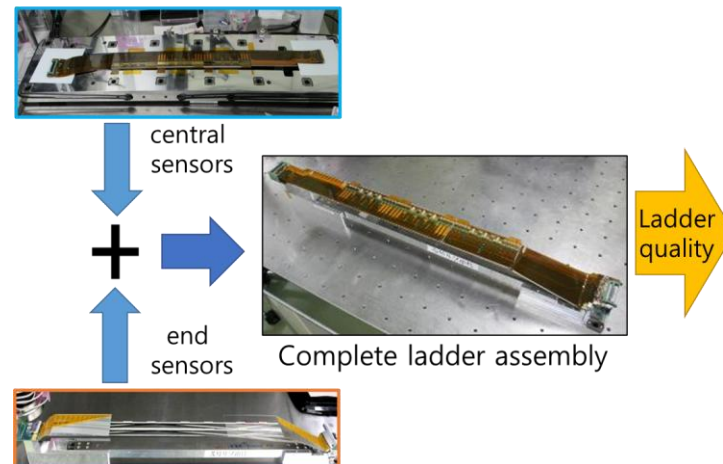
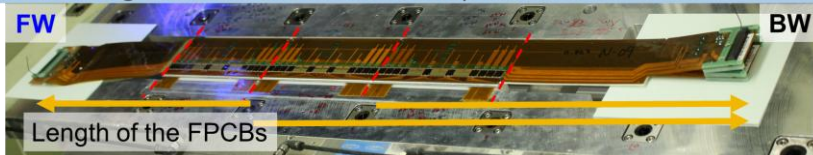
## Assembly Procedure



## Align the DSSDs on the alignment base jig.



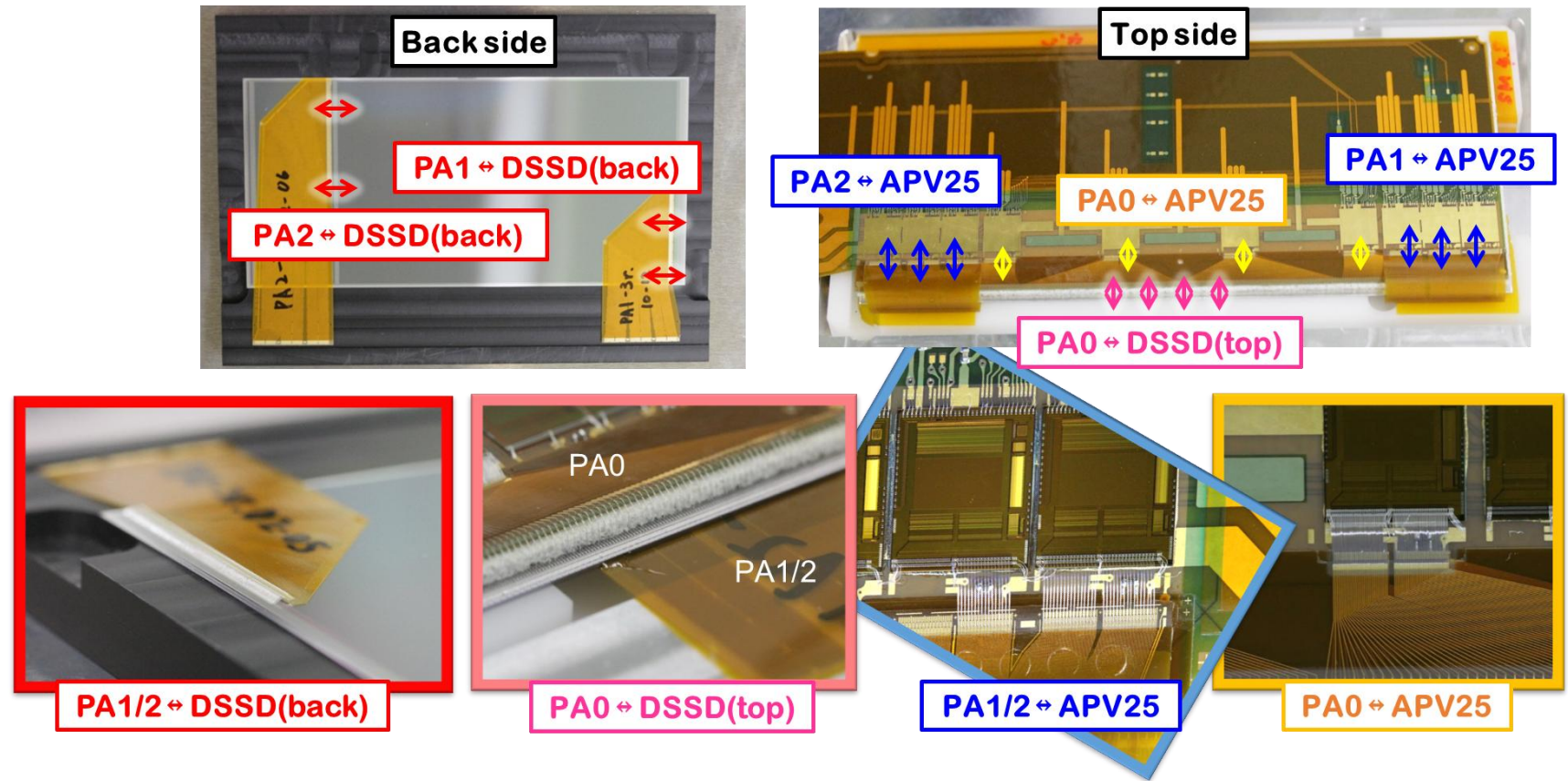
Attach the thermal insulator and the Origami FPCBs, and then perform wire-bonding between the FPCBs and top side of the DSSDs.





# Design and Construction

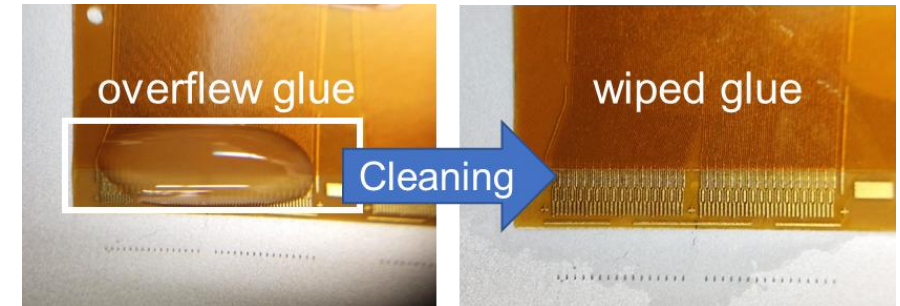
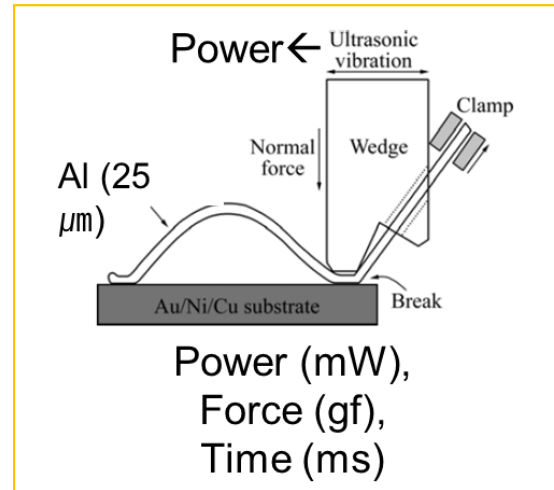
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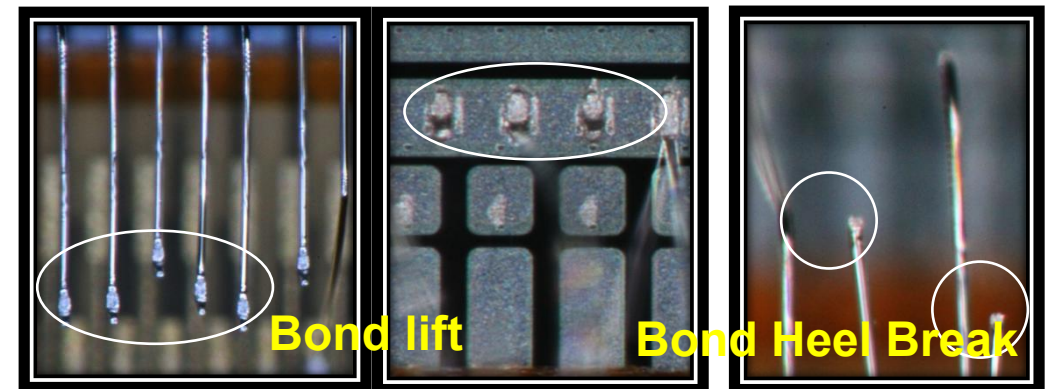
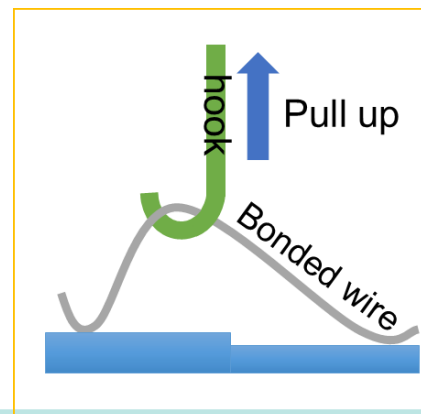


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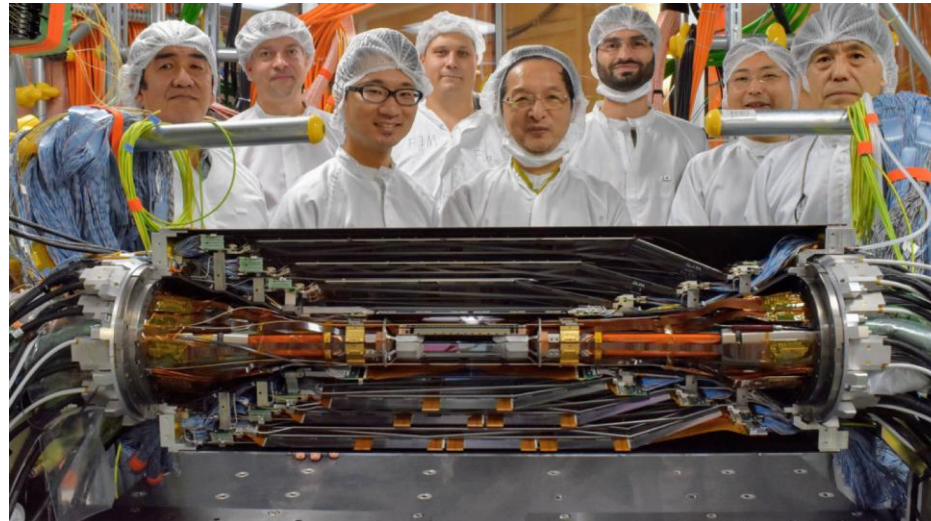
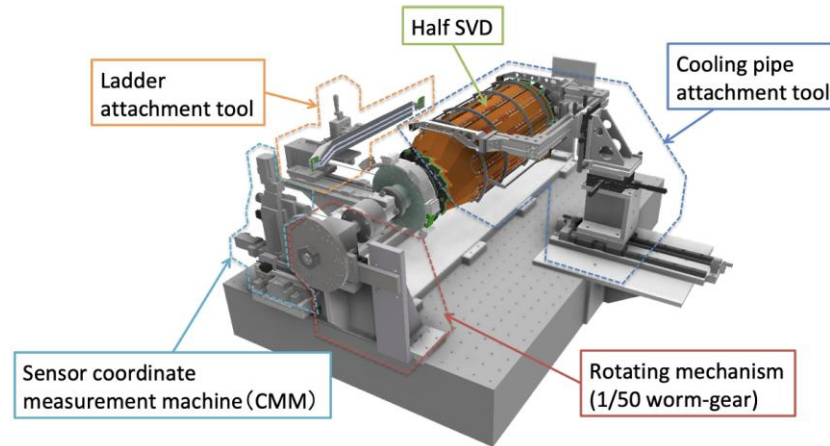


Optimized bonding parameters and gluing quality led to a 99% bonding efficiency  
- validated by pull-force measurements and bond pad inspections.



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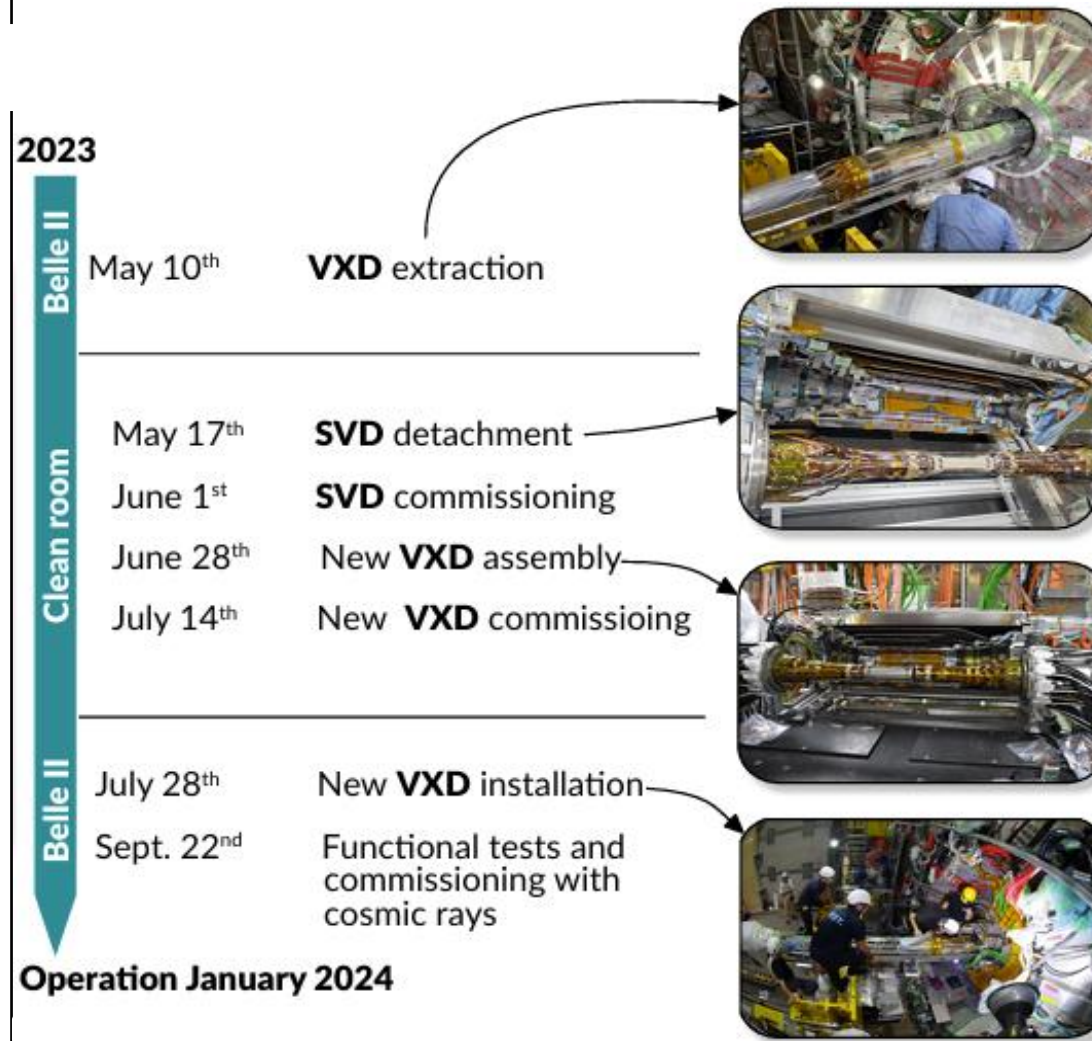


- **2008**: first Origami chip-on sensor concept
- **2010**: Belle II Technical design report
- **2012**: ladder R&D
- **2016**: ladder mass production
- **2018**: 1<sup>st</sup>/2<sup>nd</sup> SVD half-shell assembled & installed in Belle II
- **2019**: first beam collision with complete detector



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- **Re-installation @ LS1** →



Upgrade VXD with a new PXD

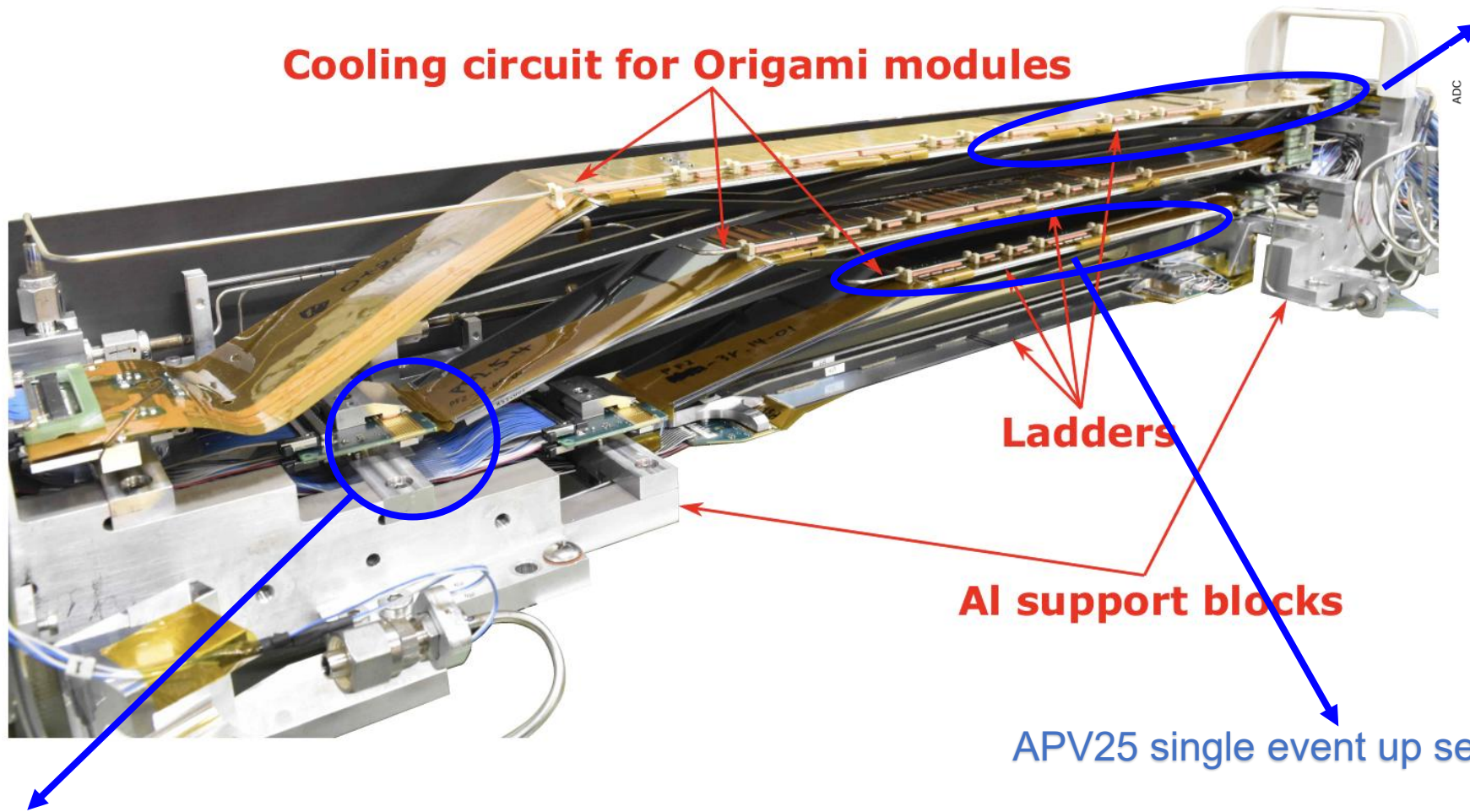
First time performing de-installation of VXD

Several SVD test campaigns to promptly **spot problems** and **sanity check**

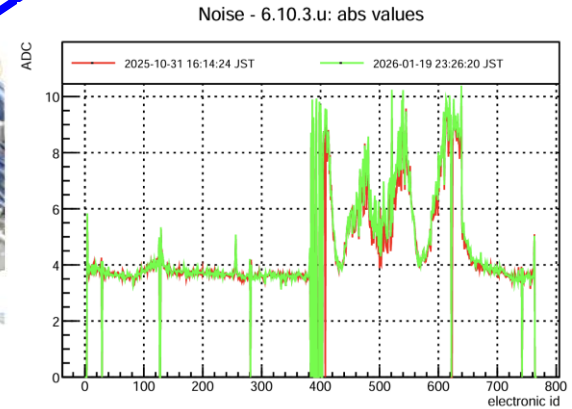
The reinstalled detector  
→ **demonstrated good performance**



# Design and Construction - issues

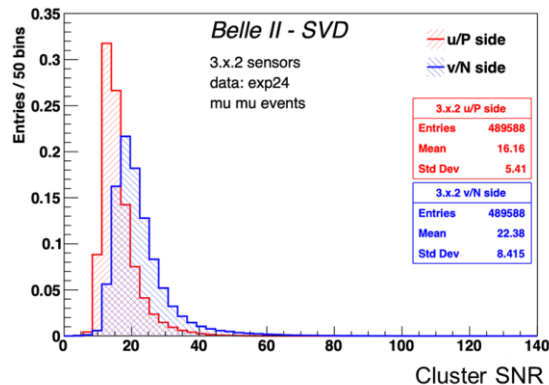


Crosstalk issue



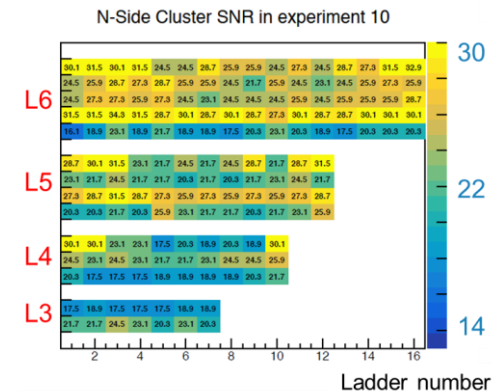
FWD PA peel off issue





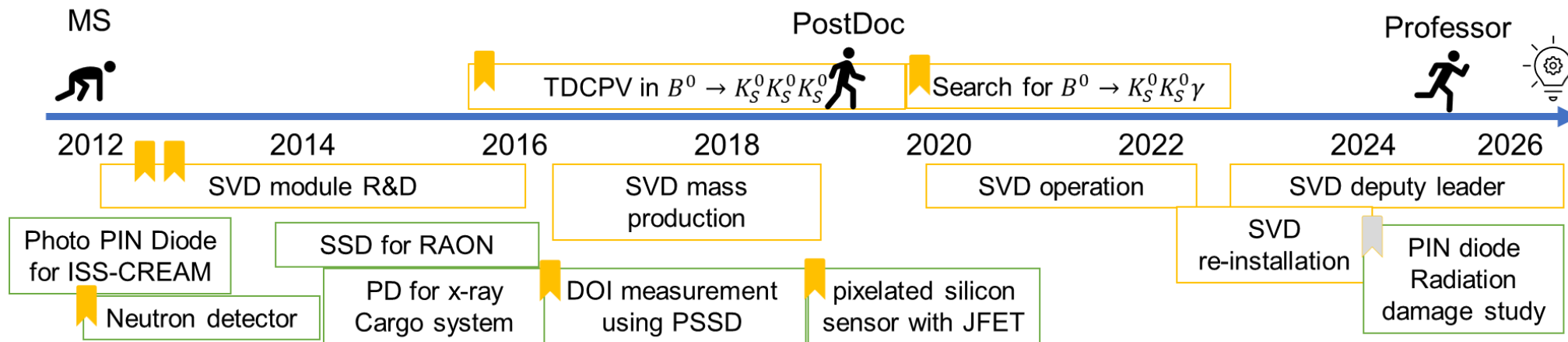
# SVD operation & performance

to be presented  
next Particle Detector Workshop 2026



# Summary

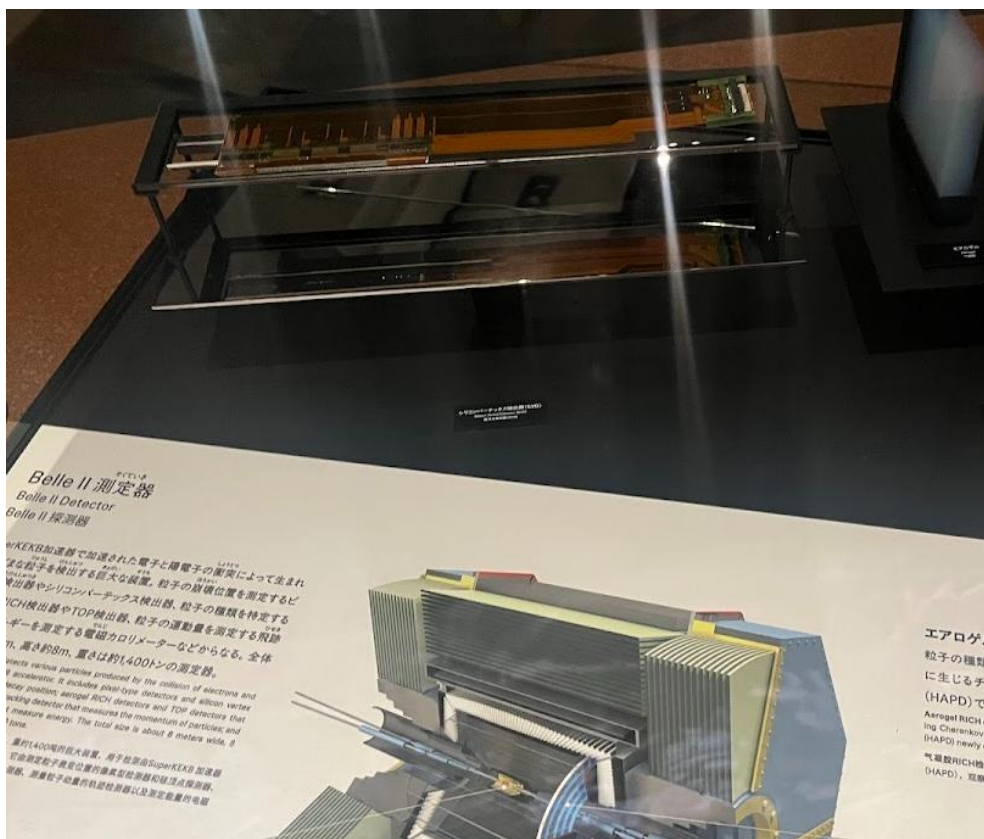
- The **SVD plays a crucial role** in Belle II and has been one of the most stable and best-performing detector.
  - **Essential contributions of electronics and mechanical engineers and technicians.**
- Radiation effects observed so far remain moderate, and the SVD performance is expected to be maintained up to the **VTX upgrade @2032**
- Over more than 10 years of involvement in the SVD, we (I) have accumulated extensive experience in design, construction, operation, and performance.
  - The SVD technical paper published in JINST (JINST 17 P11041 2022)



Belle II – SVD  
& VTX upgrade  
EIC – BIC  
LGAD for x-ray  
+ ???



# 2026 일본 과학 미래관



2024 summer

